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Gordon Edward Morrison et al.

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U.S. PATENT DOCUMENTS

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ee	AA	3 34 3 13 5	9-19-57	Freiman, et al.	340	172.5	
ee	AB	3 61 1 30 6	10-5-71	Reigel	340	172.5	
ee	AC	3 77 1 14 1	11-6-73	Culler	340	172.5	
ee	AD	4 10 4 72 0	8-1-78	Gruner	364	200	
ee	AE	4 10 9 31 1	8-22-78	Blum, et al.	364	200	
ee	AF	4 15 3 93 2	5-8-79	Dennis, et al.	364	200	
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ee	AK	4 27 0 16 7	5-26-81	Koehler, et al.	364	200	

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							YES NO
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ee	AR	Dennis, "Data Flow Supercomputers", Computer, November, 1980, Pgs. 48-56
ee	AS	Hagiwara, H. et al., "A Dynamically Microprogrammable, Local Host Computer With Low-Level Parallelism", IEEE Transactions on Computers, C-29, N-7, July, 1980, Pgs. 577-594.
ee	AT	Fisher et al., "Microcode Compaction: Looking Backward and Looking Forward", National Computer Conference, 1981, Pgs. 93-102.

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EC	BA XX	4 43 5 75 8	3-6-84	Lorie, et al.	364	200	
EC	BB XX	4 46 8 73 6	8-28-84	DeSantis	364	200	
EC	BB XX	4 46 6 06 1	8-14-84	DeSantis	364	200	
	BD						
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EC	BR XX	Fisher et al., "Using an Oracle to Measure Potential Parallelism in Single Instruction Stream Programs", IEEE No. U194-1895/81/0000/0171 14th Annual Microprogramming Workshop, Sigmicro, October, 1981, Pgs. 171-182.
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CR XX	Bernhard, "Computing at the Speed Limit", IEEE Spectrum, July, 1982, Pgs. 26-31.
CS XX	Davis, "Computer Architecture", IEEE Spectrum, November, 1983, Pgs. 94-99.
CT XX	Hagiwara, H. et al., "A User-Microprogrammable Local Host Computer With Low-Level Parallelism", Article, Association for Computing Machinery, #0149-7111/83/0000/0151, 1983, Pgs. 151-157.

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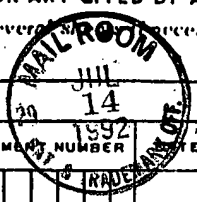
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ce	DR		
	XX	McDowell, Charles Edward, "SIMAC: A Multiple ALU Computer", Dissertation Thesis, University of California, San Diego, 1983 (111 pages).	✓
ce	DS		
	XX	McDowell, Charles E., "A Simple Architecture for Low Level Parallelism", Proceedings of 1983 International Conference on Parallel Processing, Pgs. 472-477.	✓
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	XX	Regua, et al., "The Piecewise Data Flow Architecture: Architectual Concepts, IEEE Transactions on Computers, Vol. C-32, No. 5, May, 1983, Pgs. 425-438.	✓

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ee	ER XX	Fisher, A.T., "The VLIW Machine: A Multiprocessor for Compiling Scientific Code", Computer, 1984, Pgs. 45-52.
	ES XX	Fisher, et al., "Measuring the Parallelism Available for Very Long Instruction, Word Architectures", IEEE Transactions on Computers, Vol. C-33, No. 11, November, 1984, Pgs. 968-976.
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